

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

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Title of Invention

METHOD FOR CREATING PATTERNS FOR PRODUCING
INTEGRATED CIRCUITS

Application Number : 10/621114



Confirmation Number: 9085

First Named Applicant: Louis SCHEFFER

Attorney Docket Number: CA7035292001

Art Unit: 2825

Examiner: Naum B. Levin

Search string: (5932377 or 6579651 or 6634018 or 6670082 or 6703167 or 6787271 or
20020026626 or 20030165749 or 20040067423).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	5932377	1999-08-03	Ferguson et al.			
	2	6579651	2003-06-17	Subramanian et al.			
	3	6634018	2003-10-14	Randall et al.			
	4	6670082	2003-12-30	Liu et al.			
	5	6703167	2004-03-09	LaCour			
	6	6787271	2004-09-07	Cote et al.			

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20020026626	2002-02-28	Randall et al.			
	2	20030165749	2003-09-04	Fritze et al.			
	3	20040067423	2004-04-08	Chen et al.			

Remarks

Note: Remarks are not for responding to an office action.

Related Applications: Applicant(s) bring to the Office's attention the following related application(s): U.S. Patent Application Nos. 10/620,284 filed July 14, 2003; 10/620,285 filed July 14, 2003; and 11/143,361 filed June 1, 2005.

Signature

Examiner Name	Date